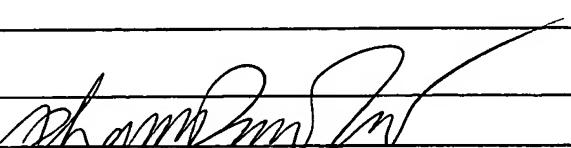


Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 248634US2DIV		SERIAL NO. New Application	
				APPLICANT Hiroyuki KOBAYASHI, et al.			
LIST OF REFERENCES CITED BY APPLICANT				FILING DATE Herewith		GROUP Unassigned	
		U.S. PATENT DOCUMENTS					
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<i>LJ</i>	AA	5,708,599 A	01/1998	Sato et al			
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FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION		
					YES	NO	
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>LJ</i>	AW	Fariborz ASSADERAGHI, et al. "A Dynamic Threshold Voltage MOSFET (DTMOS) for Very Low voltage Operation", IEEE ELECTRON DEVICE LETTERS, Vol. 15, No. 12, Dec. 1994, pgs 510-512					
<i>LJ</i>	AX	In-Young Chung, et al. "A New SOI Inverter Using Dynamic Thershould for Low-Power Applications", IEEE ELECTRON DEVICE LETTERS, Vol. 18, No. 6, June 1997, pgs 248-250					
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner 				Date Considered <i>06/10/2007</i>			
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							